

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Martin C. Roberts et al.

Title: METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY-PROCESS

Docket No.: 303.451US6

Serial No.: 09/745,780

Filed: December 21, 2000

Due Date: August 18, 2005

Examiner: W. David Coleman

Group Art Unit: 2823

MS Appeal Brief - Patents

Commissioner for Patents

P.O. Box 1450

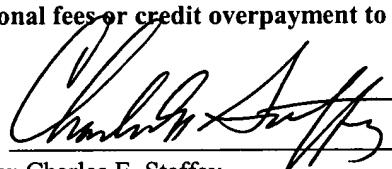
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

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 Appeal Brief (23 pgs.).
 Petition for Extension of Time (1 pg.)

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

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